

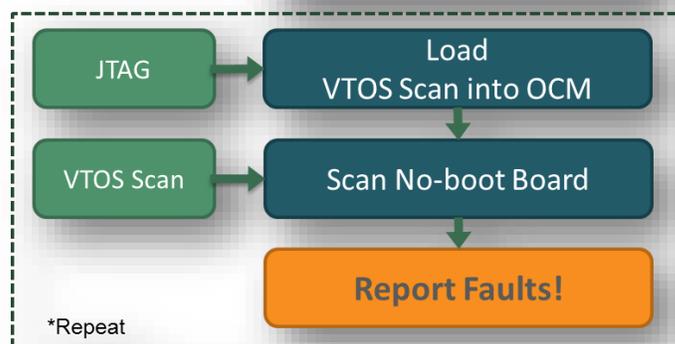
VTOS Scan™ Provides

- Firmware that is ready to run on your custom circuit board, requiring only a working CPU with a JTAG interface.
- Friendly, task-focused user interface that walks you through debugging non-booting boards.
- Automatic test generation from known-good boards.
- Identification of faulty components using your custom PCB reference designators.
- Robust scanning of I2C, PCI/PCIe, Ethernet PHYs, Flash Memory, GPIO, SD/MMC, SP, and USB.
- Extensible, scriptable scanning engine.
- Easy to read output for humans or parsers.
- Fast code execution and scan results.

VTOS Scan™ Process Flow

Follow these steps to detect faults on a device under test:

- VTOS Scan is ready for use via a software downloads page. A license is required for full product activation.
- Using JTAG, load VTOS Scan into on-chip memory.
- Using the VTOS Scan UI, scan a working board to auto-generate tests.
- For each device under test, load VTOS Scan using JTAG and execute tests to detect and report faults.



General Features/Benefits

- No firmware development required
- Load onto any circuit board using a supported SoC
- Fast and simple to use

Production Test Benefits

- Extremely fast testing and scanning
- Quickly scans and verifies the operational capabilities of a printed circuit board
- Run on bare boards, partial assemblies, or fully assembled units

Additional Product Information

VTOS Scan requires a supported processor in working condition, a UART or JTAG communication channel, and at least 32 KIB of on-chip memory (requirement varies by SoC).

For additional information:

- Email sales@kozio.com
- Or call +1 303-776-1356 x1

No-boot Board Debugging

- Runs from on-chip memory, insulating debug from faulty devices
- Instant recovery from processor and device faults
- Run multiple scans and tests without reboots

Reporting

- Consistent reporting for manufacturing and post-processing
- Scan results include: bus type, bus index, devices found, and a detailed description of each component discovered
- Component reports include a user-configurable ID field that can be mated to PCB reference designators