

## Overview

The PXI XJLink2 modules allow the integration of XJTAG into PXI-based test systems. PXI XJLink2 has one JTAG controller and PXI XJLink2 Duo has two JTAG controllers. Each controller has 4 TAPs, which are configurable for pinout and voltage. PXI XJLink2 modules are easily integrated with LabVIEW™ with a full set of Virtual Instruments (VIs) included.

### PXI/cPCI solution

PXI XJLink2 and PXI XJLink2 Duo are CompactPCI® compatible so they can be used in any cPCI or PXI-based test system.

### Configurable JTAG interface

Only one simple cable assembly is required to connect to your target board — no extra adapters needed. The 20-way connectors on PXI XJLink2 and PXI XJLink2 Duo are configurable to match the connector pinout on the PCB. The ability to change the pinmap for the JTAG signals simplifies the process of connecting your XJTAG test system to the Unit Under Test.

### Integration

The standard .NET and COM interfaces allow you to integrate XJTAG testing with test executives such as NI LabVIEW™, LabWindows™/CVI, with ATE machines, or a bespoke application written in languages such as Visual Basic® and C#®. XJTAG provides coding examples for LabVIEW, LabWindows, C#, C++, and Visual Basic.



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### Flexible & scalable

From a single port system for development to a multipoint test/programming system in production, PXI XJLink2 modules provide a flexible, scalable JTAG solution.




With self-contained licences, all XJTAG hardware provides flexibility as no network connection is required for the test system to run.

### Advanced connectivity

PXI XJLink2 and PXI XJLink2 Duo have variable signal termination, so they can handle boards both with and without signal termination. The advanced auto-skew control enables you to get the maximum frequency out of your JTAG chain and cable.

## Key Benefits

- Standard PXI interface: ideal for lab and production work
- Modular, scalable solution
- Self-contained licence allows you to run XJTAG from a standalone PXI rack
- Re-configurable pinout for multiple UUTs saving costs
- Easy integration with  LabVIEW™

## Features

- 32bit PXI/c PCI bus interface
- NI LabVIEW Virtual Instruments available
- Up to 4 TAP connections to UUT
- Handles different cable and board configurations
- TCK clock frequencies up to 166 MHz
- Two different voltages can be configured, from 1.1 V to 3.3 V in 0.1 V steps
- Visual indication of test status
- Adjustable JTAG signal termination
- Spare pins on the JTAG connector can be used to indicate the test status or as general purpose I/O during testing, for example for fast Flash programming
- Automatic signal skew control
- Can supply power to the target board (3.3 V, <100 mA)
- Built in voltage meter on all I/O pins  
Voltage input: Min 0, Max 5 V
- Frequency counter on all I/O pins  
Frequency input: Min 1 Hz, Max 200 MHz  
Selectable measurement period of 1ms, 10ms, 100ms, 1s, 10s
- JTAG signals are +5 V tolerant

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