J-Testr

Advanced functional testing in a compact re-usable solution



The J-Testr system has been deployed with customers across the electronics industry for over 10 years. It is being used in sectors such as military, aerospace, medical, industrial and commercial performing a wide range of testing, Including:

- Advanced power and loading.
- Digital, analog and mixed signals.
- Boundary scan or no boundary scan.
- High and low volume production boards.
- Multiple size boards.

The flexible, compact and robust design of the J-Testr allows it to be used on initial prototype builds, through to volume production and post-sale service returns.



J-Testr's wiring creates **consistent** test results across multiple fixtures with:

HHH

JUT

- Efficient, short cabling.
- Predictable, consistent impedance, from source to destination.
- Wireless probe compatible for ultimate signal predictability.
- True self test exchangeable, test system and cabling to pylon.

8

J-Testr incorporates **advanced safety features** to rapidly shutdown the electronics when under fault conditions, protecting high value boards. Other **key features** include:

Multi-input oscilloscope, time and spectrum capable	Advanced power supplies and electronic loads
Advanced debugging and "open source" drivers	Intergrated USB and ethernet
Timing IO capabilities to 5ns resolution.	CAN, RS422/485, ARINC429, UARTS, SPIs, I2C
Multiple low & high voltage GPIO (JTAG capable)	Configurable HMI and safety Interlocks
Precision DAC outputs and flexible precision ADCs	Advanced boundary scan option



The **J-Testr Core** can also easily fit inside 3rd party fixtures and can be used as a standalone test system.

The exchangeables allow the system to test different UUTs at different stages of the product lifecycle:



Prototype (learning and development) Manual Plug (Low volume, low cost)





Standard (Bed of nails applications)

J-Testr communications is software agnostic; it can be controlled by any software with a

J-Testr self-test (True test system validation)



socket interface and uses a simple ASCII protocol. It features **advanced "open-source" debugging tools.** The interface is similar to a micro controller device and is

controlled via memory mapped 16-bit registers sets.

J-Testr can be enabled to perform

advanced **boundary scan** testing, with seamless integration, via an easy to attach / remove module. It has impedance-controlled TAP signals to UUT and J-Testr IOs.





J-Testr is available **off-the-shelf**, for customers with test development teams. Configuration options are available to meet specific customer needs.

J-Testr is also available as a **full turnkey solution**, developed to a structured development plan by an experienced multidisciplined team. Turnkey services include bespoke electronics, hardware, software and mechanics and is delivered with full design data and fully tested.

Both offerings come with customer support and comprehensive documentation.